zxw RISC data path design

Design the data path from which is using pc realative, multiplexer, 3buses, memory mapped, micro programmed and Harvard. The design is modification of lecture design which uses 1bus, tri state and others. Then complete ASM chart and control signal table from the design.

For 3 buses, there is no temporary register and no enable A and B registers. The tri state is similar to multiplexer as they need to select the function the way that will work. By adding memory mapped, Harvard designs, the work is almost complete. ASM chart is completed by figuring the hand-out table and logic. Control signal table is simple if we understand ASM chart

I finish all work including design, ASM chart and control signal table.

There is no problem remaining to be solved